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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet

3

of

3

Application Number

10/766,116

Filing Date

January 27, 2004

First Named Inventor

Guterman

Art Unit

2827

Examiner Name

V. Q. Nguyen

Attorney Docket Number

SAND-01016US0

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
VN		KURATA, Hideaki, et al., Constant-Charge-Injection Programming for 10-MB/s Multilevel AG-AND Flash Memories, 2002 Symposium On VLSI Circuits Digest of Technical Papers, pp. 302-303	
W		JOHNSON, William S., et al., SESSION XII: ROMs, PROMs and EROMs, 1980 IEEE International Solid State Circuits Conference, pp. 152-153	
W		NOBUKATA, Hiromi, et al., A 144Mb 8-Level NAND Flash Memory with Optimized Pulse Width Programming, 1999 Symposium on VLSI Circuits Digest of Technical Papers, pp. 39-40	
W		OHKAWA, Masayoshi, et al., TP 2.3: A 98 mm ² 3.3V 64Mb Flash Memory with FN-NOR Type 4-level Cell, 1996 IEEE International Solid-State Circuits Conference, pp. 36-37	

Examiner Signature	V. Nguyen	Date Considered	9/2/05
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PTO/SB/08A (08-03)

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U. S. PATENT DOCUMENTS

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